

APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Type::	REGULAR
Subject Matter::	UTILITY
CD-ROM or CD-R?::	NONE
Title::	METHOD FOR COMPRESSING SEMICONDUCTOR INTEGRATED CIRCUIT, USING DESIGN REGION DIVIDED INTO PLURAL BLOCKS
Attorney Docket Number::	243650US2S
Total Drawing Sheets::	15

INVENTOR INFORMATION

Applicant Authority Type::	INVENTOR
Primary Citizenship Country::	Japan
Status::	FULL CAPACITY
Given Name::	Naoyuki
Family Name::	TAMURA
City of Residence::	Kawasaki-shi
Country of Residence::	Japan
Street of Mailing Address::	c/o Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura 1-chome, Minato-ku
City of Mailing Address::	Tokyo
Country of Mailing Address::	Japan
Postal or Zip Code of Mailing Address::	105-8001

Applicant Authority Type:: INVENTOR
 Primary Citizenship Country:: Japan
 Status:: FULL CAPACITY
 Given Name:: Takayuki
 Family Name:: KAMEI
 City of Residence:: Yokohama-shi
 Country of Residence:: Japan
 Street of Mailing Address:: c/o Intellectual Property Division, Toshiba Corporation, 1-1, Shibaura 1-chome, Minato-ku
 City of Mailing Address:: Tokyo
 Country of Mailing Address:: Japan
 Postal or Zip Code of Mailing Address:: 105-8001

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

FOREIGN PRIORITY INFORMATION

Application Number:	Country::	Filing Date::	Priority Claimed::
2003-199140	Japan	07/18/03	YES